

Application No.	Applicant(s)	
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Examiner	Art Unit	
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Reason for allowance

1. This communication is in response to applicant's 03/07/2005 Amendment in the application of Ito et al. for a "Packet transmission/reception" filed 04/20/2001. This application claims foreign priority based on the application 2000-124867 filed April 25, 2000 in Japan. The proposed amendment to the claims and response have been entered and made of record. Claims 1, 9-10 have been canceled per Applicant's request, and claims 2-4, 6, 8 have been amended. New claims 11-17 have been added. Applicant's argument and explanation with regard to the rejection under 35 USC 103 are persuasive. The claims have been amended to better point out and more distinctly claim subject matter comprising the invention.

The corrected or substitute drawings were received on 03/07/05. These drawings are approved. The objections of record with respect to the drawings are hereby removed based on applicant's amendment.

- 2. Claims 2-8, 11-17 are allowable as evident by applicant's amendment (Claims are renumbered as 1-14 respectively).
- 3. The following is an examiner's statement of reasons for allowance: The instant application is deemed to be directed to a no obvious improvement over the prior art of record. The improvement comprises the feature to a link core circuit for receiving a packet externally delivered through a bus; a packet processing controller for processing the packet received by the link core circuit, generating a packet to be transmitted in response to the received packet, and

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supplying the packet to be transmitted to the link core circuit; a packet processing control timer

for clocking how much time has passed since the packet was received by the link core circuit and

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for generating a signal when the time passed reaches a predetermined amount of time, wherein

the link core circuit sends out the packet to be transmitted, supplied from the packet processing

controller, by way of the bus, and responsive to the signal supplied from the packet processing

control timer, the packet processing controller stops processing the received packet, generates

another packet to be transmitted in response to the received packet and restarts processing the

received packet, as specifically recited in the claims.

Any comments considered necessary by applicant must be submitted no later than the 4.

payment of the issue fee and, to avoid processing delays, should preferably accompany the issue

fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for

Allowance."

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's

disclosure.

The Muto (US#6,580,711) is cited to show the serial interface circuit and signal

processing method of the same.

The Williams (US#6,205,494) is cited to show the controller with automatic generation

of linked list of data transfer descriptors for sequential commands, with linked list being used for

execution of sequential data transfers.

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The Tachibana (US#2005/0163120) is cited to show the data processing circuit.

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The Hanaoka et al. (US#6,457,079) is cited to show the communication apparatus with means for allocating alternate designation information to each function unit, and communication system with said two communication apparatuses.

The Hata (US#2001/0017860) is cited to show the communication control device and method.

The Yumiba et al. (US#6,654,380) is cited to show the data exchange unit.

The Ishimura et al. (US#6,693,905) is cited to show the data exchange unit.

- 6. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."
- 7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Man U. Phan whose telephone number is (571) 272-3149. The examiner can normally be reached Monday through Friday from 6:00 am to 3:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Huy Vu, can be reached on (571) 272-3155. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (571) 272-2600.

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8. Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published

applications may be obtained from either Private PAIR or Public PAIR. Status information for

unpublished applications is available through Private PAIR only. For more information about

the PAIR system, see http://pair-direct.uspto.gov. Should you have any questions on access to

the Private PAIR system, contact the Electronic Business Center (EBC) at toll free 1-866-217-

9197.

MPhan

Aug. 02, 2005

Man u. Phan MAN U. PHAN PRIMARY EXAMINER